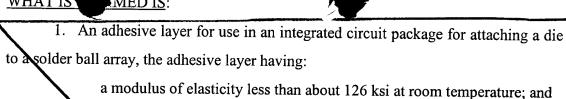
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- a coefficient of thermal expansion (CTE) of less than about 200 ppm/°C.

 2. The adhesive layer of Claim 1, wherein the die is selected from the group consisting of microprocessors, co-processors, digital signal processors, graphics processors, microcontrollers, memory devices, reprogrammable devices, programmable logic devices, and logic arrays.
- 3. The adhesive layer of Claim 1, wherein the modulus of elasticity is greater than about 10 ksi.
- 4. The adhesive layer of claim 2, wherein the modulus of elasticity is greater than about 50 ksi.
- 5. The adhesive layer of Claim 4, wherein the modulus of elasticity is greater than about 100 ksi.
- 6. The adhesive layer of Claim 1, wherein the coefficient of thermal expansion is less than about 150 ppm/°C.
- 7. The adhesive layer of Claim 6, wherein the coefficient of thermal expansion is less than about 100 ppm/°C.
 - 8. An integrated circuit package, comprising:

a die;

a die attach layer over the die; and

an array of solder balls over the die attach layer;

wherein the die attach layer has a coefficient of thermal expansion of less than about 106 ppm/°C.

- 9. The integrated circuit package of Claim 8, further comprising a flexible tape connecting the array of solder balls to the die.
- 10. The integrated circuit package of Claim 8, wherein the die attach layer has a thickness of between about 5 and 7 mils.
- 30 11. The integrated circuit package of Claim 8, wherein the die attach layer is an epoxy modified with elastomeric material.

Sul	/	12 integrated circuit package of Clain wherein the package includes a ball grid array.
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$\bigvee_{i,j}$		13. The integrated circuit package of Claim 8, wherein the package includes a
O		tape ball grid array.
	5	14. The integrated circuit package of Claim 8, wherein the package includes a
		micro ball grid array.
		15. An integrated circuit package, comprising:
		a die;
		a die attach layer over the die; and
	10	an array of solder balls over the die attach layer;
		wherein the die attach layer has a coefficient of thermal expansion of less
		than about 106 ppm/°C and a modulus of elasticity of less than about 126 ksi.
	2	16. The integrated circuit package of Claim 15, further comprising a flexible
	. 1	tape connecting the array of solder balls to the die.
	11 15	17. A first level integrated circuit package, comprising:
		a chip;
		an array of solder balls for connecting the first level package to a second
		level package;
		an adhesive layer between the chip and the array of solder balls, the
	20	adhesive layer having a coefficient of thermal expansion of less than about 200
<u></u>		ppm/°C; and
		a flexible tape connecting the array to the chip.
		18. The package of Claim 17, wherein the tape connects the array to the chip
		using µBGA technology.
	25	19. The package of Claim 17, wherein the adhesive layer has a coefficient of
		thermal expansion of less than about 150 ppm/°C.
		20. The package of Claim 17, wherein the adhesive layer has a coefficient of
		thermal expansion of less than about 100 ppm/°C.
< 1L	<i>)</i>	21. A first level integrated circuit package, comprising:
SW	3 0	a chip;
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•		

Λ.)	an array of solder balls for connecting first level package to a second
Sul	ر م	level package;
1	0	an adhesive layer between the chip and the array of solder balls, the
O.		adhesive layer having a coefficient of thermal expansion of less than about 200
	5	ppm/°C; and
		a flexible tape connecting the array to the chip;
		wherein the adhesive layer has a modulus of elasticity of greater than
		about 10 ksi and less than about 126 ksi.
		22. The package of Claim 21, wherein the adhesive layer has a modulus of
	10	elasticity of greater than about 50 ksi.
		23. The package of Claim 22, wherein the adhesive layer has a modulus of
		elasticity of greater than about 100 ksi.
		24. A method of preventing breakage of a flexible tape bonded to a chip and
The state of the s		connected to an array of solder balls, comprising:
	15	providing a compliant material between the chip and array of solder
		balls, the material having a coefficient of thermal expansion of less than about
		106 ppm/°C and a modulus of elasticity of less than about 126 ksi.
Sul)	25. An integrated circuit package, comprising:
= 1 D		
	6	a flexible substrate;
	b 20	a flexible substrate; a chip:
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The hand of the second	20	a chip:
	20	a chip: a plurality of conductive terminals on the substrate;
	20	a chip: a plurality of conductive terminals on the substrate; a plurality of conductive leads electrically connecting the conductive
	20	a chip: a plurality of conductive terminals on the substrate; a plurality of conductive leads electrically connecting the conductive terminals to the chip; and
		a chip: a plurality of conductive terminals on the substrate; a plurality of conductive leads electrically connecting the conductive terminals to the chip; and a compliant material between the chip and the substrate, the compliant
		a chip: a plurality of conductive terminals on the substrate; a plurality of conductive leads electrically connecting the conductive terminals to the chip; and a compliant material between the chip and the substrate, the compliant material having a modulus of elasticity of less than about 126 ksi at room
		a plurality of conductive terminals on the substrate; a plurality of conductive leads electrically connecting the conductive terminals to the chip; and a compliant material between the chip and the substrate, the compliant material having a modulus of elasticity of less than about 126 ksi at room temperature and a coefficient of thermal expansion of less than about 200 ppm/°C. The integrated circuit package of Claim 25, wherein the flexible substrate is a polyimide.
	25	a chip: a plurality of conductive terminals on the substrate; a plurality of conductive leads electrically connecting the conductive terminals to the chip; and a compliant material between the chip and the substrate, the compliant material having a modulus of elasticity of less than about 126 ksi at room temperature and a coefficient of thermal expansion of less than about 200 ppm/°C. The integrated circuit package of Claim 25, wherein the flexible substrate is a polyimide. 26. The integrated circuit package of Claim 25, wherein the plurality of
		a chip: a plurality of conductive terminals on the substrate; a plurality of conductive leads electrically connecting the conductive terminals to the chip; and a compliant material between the chip and the substrate, the compliant material having a modulus of elasticity of less than about 126 ksi at room temperature and a coefficient of thermal expansion of less than about 200 ppm/°C. The integrated circuit package of Claim 25, wherein the flexible substrate is a polyimide. 26. The integrated circuit package of Claim 25, wherein the plurality of conductive terminals includes an array of solder balls.
	25	a chip: a plurality of conductive terminals on the substrate; a plurality of conductive leads electrically connecting the conductive terminals to the chip; and a compliant material between the chip and the substrate, the compliant material having a modulus of elasticity of less than about 126 ksi at room temperature and a coefficient of thermal expansion of less than about 200 ppm/°C. The integrated circuit package of Claim 25, wherein the flexible substrate is a polyimide. 26. The integrated circuit package of Claim 25, wherein the plurality of conductive terminals includes an array of solder balls. 27. The integrated circuit package of Claim 25, wherein the plurality of
	25	a chip: a plurality of conductive terminals on the substrate; a plurality of conductive leads electrically connecting the conductive terminals to the chip; and a compliant material between the chip and the substrate, the compliant material having a modulus of elasticity of less than about 126 ksi at room temperature and a coefficient of thermal expansion of less than about 200 ppm/°C. The integrated circuit package of Claim 25, wherein the flexible substrate is a polyimide. 26. The integrated circuit package of Claim 25, wherein the plurality of conductive terminals includes an array of solder balls.